

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hiroshi AKASAKI et al.

Appln. No.:

Filed: HERewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

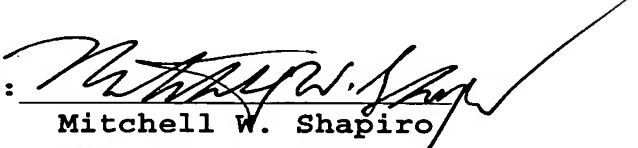
Applicants wish to make of record the documents cited
in prior Application No. 10/153,525 filed May 24, 2002,
whether cited by Applicants or by the Patent Office. The
documents are listed on the attached Form PTO-1449.

Respectfully submitted,

MWS:sjk

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102-3833
(703) 903-9000

By:


Mitchell W. Shapiro
Reg. No. 31,568

April 14, 2004

FORM PTO-1449				Atty. Docket No. XA-9665A		Appln. No.	
<u>LIST OF DOCUMENTS CITED BY APPLICANT</u>				Applicant Hiroshi AKASAKI et al.			
				Filing Date HEREWITH		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA	6,417,715	07/09/02	Hamamoto et al.	327	291	
	AB	6,229,757	05/08/01	Nagata et al.	365	233	
	AC	6,125,064	09/26/00	Kim et al.	365	193	
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	6-52676	02/25/94	Japan			Yes
	AK						
	AL						
	AM						
	AN						
	AO						
OTHER (including author, title, date, pertinent pages, etc.)							
	AP						
	AQ						
	AR						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							